

HAVERSTOCK & OWENS LLP
162 North Wolfe Road
Sunnyvale, California 94086
(408) 530-9700

In re Application of: Ritu Shrivastava
Serial No.: 09/315,599
Filed: May 20, 1999
Entitled: **A METHOD OF AND APPARATUS FOR INTEGRATING FLASH EPROM AND SRAM CELLS
ON A COMMON SUBSTRATE**

Assistant Commissioner for Patents
Washington, D.C. 20231

Sir:

Transmitted herewith is an amendment in the above-identified application. The fee has been calculated as shown below.

	(Col. 1)		(Col. 2)	(Col. 3)		
	CLAIMS REMAINING AFTER AMENDMENT		HIGHEST NUMBER PREVIOUSLY PAID FOR	PRESENT EXTRA	RATE	ADDITIONAL FEE
Total Claims	* 14	MINUS	** 20	0	18.00	0.00
Independent Claims	* 4	MINUS	*** 5	0	84.00	0.00
_ First Presentation Of Multiple Dependent Claim					280.00	
TOTAL						0.00

- * If the entry in Col. 1 is less than the entry in Col. 2, write "0" in Col. 3.
** If the "Highest Number Previously Paid For" IN THIS SPACE is less than 20, write "20" in this space.
*** If the "Highest Number Previously Paid For" IN THIS SPACE is less than 3, write "3" in this space.
The "Highest Number Previously Paid For" (Total or Independent is the highest number found from the equivalent box in Col. 1 of a prior amendment or the number of claims originally filed.)

1. X No additional fee is required.
2. X Please charge any additional fees, including any fees necessary for extensions of time, or credit overpayment to Deposit Account No. 08-1275. An originally executed duplicate of this transmittal is enclosed for this purpose.

Dated: January 7, 2003

By: Jonathan O. Owens
Jonathan O. Owens
Registration No.: 37,902

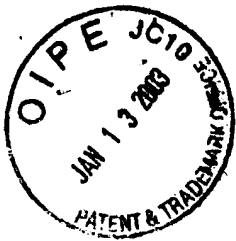
CERTIFICATE OF MAILING

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Assistant Commissioner for Patents, Washington, D.C. 20231, on January 7, 2003.

Dated: January 7, 2003

By: Sharon McCormick
Sharon McCormick

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#18/Amend C (HE)
PATENT
Atty. Docket No.: ALSC-00300 1-22-03

C. Moore

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re Application of

Shrivastava

Serial No. 09/315,599

Filed: May 20, 1999

For: **METHOD AND APPARATUS
FOR INTEGRATING FLASH
EEPROM AND SRAM CELLS
ON A COMMON SUBSTRATE**

Group Art Unit: 2814

Examiner: Ha, N.

**RESPONSE TO OFFICE ACTION
MAILED ON November 8, 2002**

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Sunnyvale, CA 94086
(408) 530-9700

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Assistant Commissioner for Patents
Washington, D.C. 20231

Sir:

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Marku
3/18/03*

AMENDMENTS

Please amend the Specification of the above-referenced patent application as follows.

Clean version of the Specification:

Page 8, lines 17-18

More specifically, the areas covered by the layer of nitride 120 will not react and will not oxidize during the steps of the STI process and the LOCOS process.

CERTIFICATE OF MAILING (37 CFR § 1.8(a)),

I hereby certify that this paper (along with any referred to as being attached or enclosed) is being deposited with the U.S. Postal Service on the date shown below with sufficient postage as first class mail in an envelope addressed to the: Assistant Commissioner for Patents, Washington D.C. 20231

HAVERSTOCK & OWENS LLP.

Date: 1/27/03 By: *Shawn McCormick*